

Figure 1  
(Prior Art)

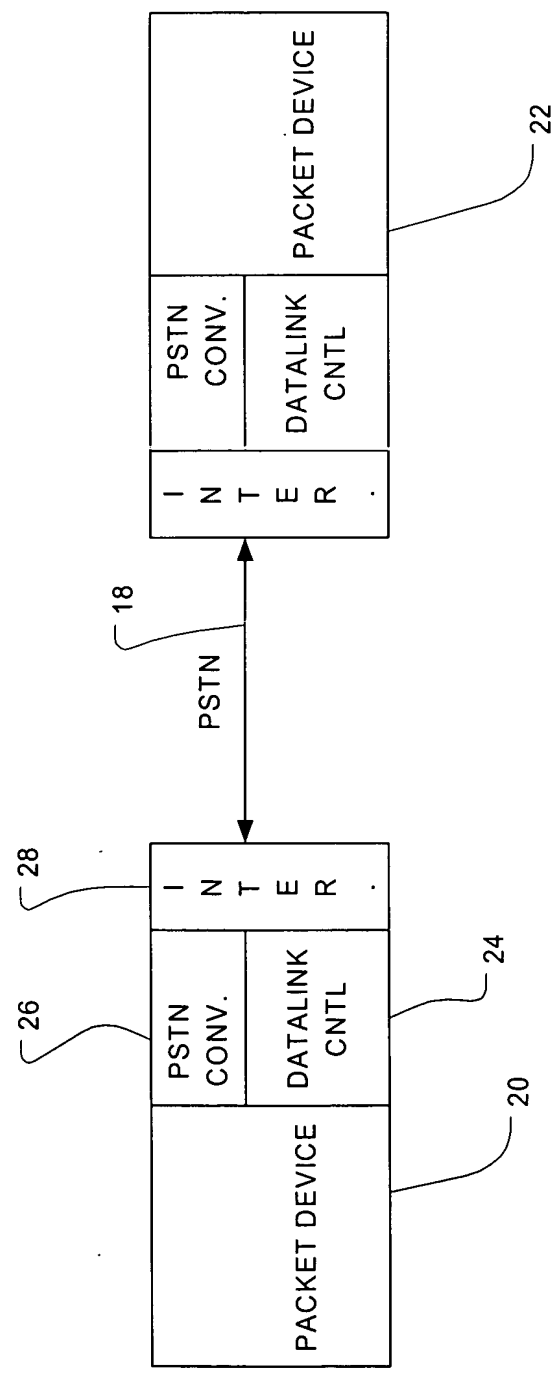


Figure 2

FIG. 3 is a block diagram of a system 20 for processing signals received from a PSTN 30 and for transmitting signals to a PSTN 38. The system 20 includes an interface 32, a signal processing unit 34, and a PSTN converter 36. The interface 32 is connected to the signal processing unit 34 and the PSTN converter 36. The signal processing unit 34 is connected to the PSTN converter 36. The PSTN converter 36 is connected to the PSTN 38. The system 20 is also connected to a PSTN 30.

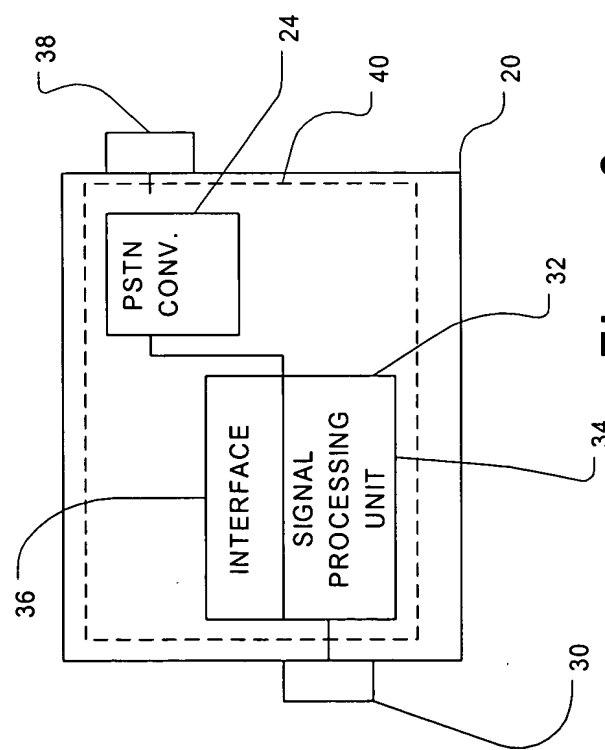


Figure 3

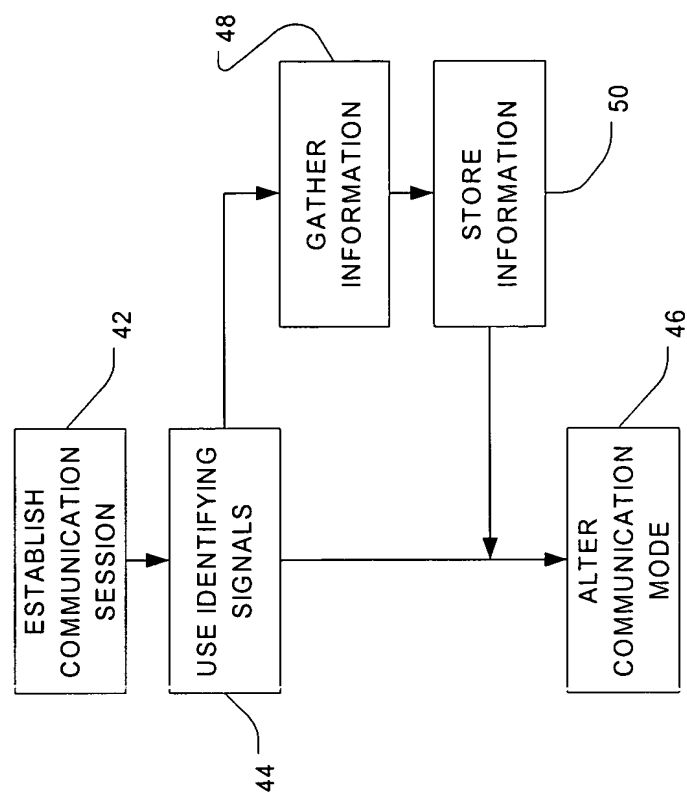


Figure 4